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Electronic Patent Application Submission  
USPTO Use Only

EFS ID: 66720  
Application ID: 09387109  
Title of Invention: SDRAM CONTROLLER FOR  
PARALLEL PROCESSOR  
ARCHITECTURE  
First Named Inventor: Matthew Adiletta  
Domestic/Foreign Application: Domestic Application  
Filing Date: 1999-08-31  
Effective Receipt Date: 2004-08-16  
Submission Type: Information Disclosure  
Statement  
Filing Type:  
Confirmation number: 2177  
Attorney Docket Number: 10559-079001



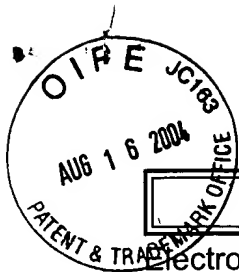
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Technology Center 2100

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# TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE																		
<table border="0"><tr><td>Application Number:</td><td>09/387109</td><td></td><td rowspan="4"><b>RECEIVED</b> AUG 19 2004 Technology Center 2100</td></tr><tr><td>Date:</td><td>1999-08-31</td><td></td></tr><tr><td>First Named Applicant:</td><td colspan="2">Matthew J. Adiletta</td></tr><tr><td>Confirmation Number:</td><td colspan="2">2177</td></tr><tr><td colspan="3">Attorney Docket Number: 10559-079001</td><td></td></tr></table>			Application Number:	09/387109		<b>RECEIVED</b> AUG 19 2004 Technology Center 2100	Date:	1999-08-31		First Named Applicant:	Matthew J. Adiletta		Confirmation Number:	2177		Attorney Docket Number: 10559-079001			
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Scott C. Harris Registered Number: 32,030	/SCH/	Attorney																	
Documents being submitted		Files																	
us-ids		10559-079001-usidst.xml																	
		us-ids.dtd																	
		us-ids.xsl																	
Comments																			